

40G QSFP+ SR4 150m Transceiver

P/N: WST-QSFP+SR4-C



Features:

- Hot Pluggable QSFP form factor
- Support 10.3125Gb/s per channel
- Low Power Dissipation, Max 1.3W Each End
- Operating Case Temperature: 0°C ~70°C
- Single MPO12 receptacle
- Link length of 100m on OM3 Multimode Fiber (MMF) and 150m on OM4 MMF
- SFF-8636 Management Interface
- SFF-8661: Pluggable Module
- SFF-8679: General Electrical
- GR-468: Reliability Qualification
- IEEE 802.3ba: Physical Layer Specifications and Management Parameters
- ROHS-6: Environment Safety

Applications:

- Ethernet for 40GBASE-SR4
- InfiniBand SDR, QDR, DDR, FDR

General Product Characteristics

Parameter	Value	Unit	Comments
Module Form Factor	QSFP	As defined by SFF-8661	Module Form Factor
Number of Lanes	4 TX and 4 RX		
Maximum Aggregate Data Rate	41.25	Gb/s	
Maximum Data Rate per Lane	10.3125	Gb/s	
Protocols Supported	InfiniBand, Ethernet		
Electrical Interface and Pin-out	38-pin edge connector		Pin-out as defined by SFF-8679
Maximum Power Consumption per End	1.3	Watts	Varies with output voltage swing and pre-emphasis settings

Management Interface	Serial, I2C-based, 400 kHz maximum frequency	As defined by SFF-8636
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Absolute Maximum Ratings

Exceeding the limits below may damage the optical transceiver module permanently.

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Ref.
Maximum Supply Voltage	V _{CC}	-0.5		3.6	V	
Storage Temperature	T _{sto}	-40		85	°C	
Case Operating Temperature	T _{op}	0		70	°C	
Relative Humidity	RH	0		85	%	1

Notes:

1. No-condensing.

Parameters

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Ref.
Supply Voltage	V _{CC}	3.14		3.46	V	
Power Consumption	P _{Con}			1.3	W	
Bit Rate	BR		10.3125		Gb/s	1
Bit Error Ratio	BER			10 ⁻¹²		2
Number of Lanes		4				
Management Interface		Serial, I2C-based, maximum frequency 400 kHz				3
Logic Input Voltage High	V _{ih}	2		V _{CC} +0.3	V	
Logic Input Voltage Low	V _{il}	-0.3		0.8	V	
Fiber Length: 2000MHz·km 50µm MMF (OM3)		0.5		100	m	4
Fiber Length: 4700MHz·km 50µm MMF (OM4)		0.5		150	m	4

Notes:

1. Single lane
2. PRBS= 2³¹-1 @ 10.3125Gb/s

3. As defined by SFF-8636
4. As defined by IEEE Std. 802.3ba – 2015

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Ref.
Transceiver Power Supply Current	I_{cc}			400	mA	
Transceiver Power on Initialization Time	T_{init}			2000	ms	
Transmitter at TP1a						
Differential Data Input Voltage Peak to Peak Swing	$V_{in,pp}$	200		1200	mV	
Differential Input Impedance	Z_{in}	90	100	110	Ohms	
Differential Input Return Loss	SDD11	$< -12 + 2 \times \text{SQRT}(f), f \text{ in GHz}$			dB	0.01-4.1GHz
		$< -6.3 + 13 \times \log_{10}\left(\frac{f}{5.5}\right), f \text{ in GHz}$			dB	4.1-11.1GHz
Differential to Common Mode Loss	SCD11			-10	dB	0.01-11.1GHz
Jitter Tolerance (Total)	TJ			0.4	UI	
Jitter Tolerance (Deterministic)	DJ			0.15	UI	
Receiver at TP4						
Differential Data Output Voltage Peak to Peak Swing	V_{opp}	300		800	mV	
Differential Output Impedance	Z_{out}	90	100	110	Ohms	
Differential Output Return Loss	SDD22	$< -12 + 2 \times \text{SQRT}(f), f \text{ in GHz}$			dB	0.01-4.1GHz
		$< -6.3 + 13 \times \log_{10}\left(\frac{f}{5.5}\right), f \text{ in GHz}$			dB	4.1-11.1GHz
Common Mode Output Return Loss	SCC22	$< -7 + 1.6 \times f, f \text{ in GHz}$			dB	0.01-2.5GHz
				-3	dB	2.5-11.1GHz
Output Rise and Fall time (20% to 80%)	T_r, T_f	24			ps	
Deterministic Jitter	DJ_{out}			0.38	UI	
Total Jitter	TJ_{out}			0.64	UI	

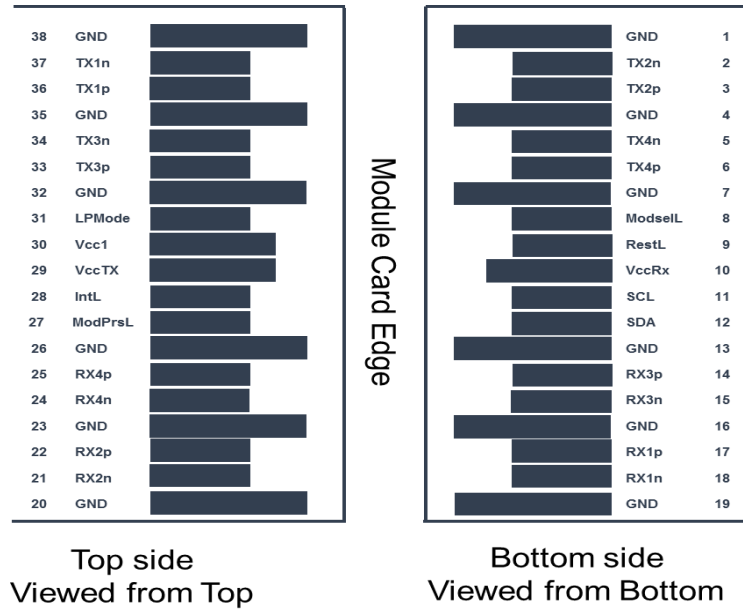
Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Ref.
Transmitter						
Signaling Speed per Lane		10.3125 ± 100ppm			Gb/s	
Lane wavelengths (Range)	λ	840		860	nm	
RMS Spectral Width	$\Delta\lambda$			0.65	nm	
Average launch power, each lane	P_{avg}	-7.6		2.4	dBm	
Optical Modulation Amplitude	OMA	-5.6		3	dBm	
Differential in Launch power between any two lanes (OMA)				4	dB	
Extinction ratio	ER	3			dB	
Transmitter and dispersion penalty, each lane	TDP			3.5	dB	
Average launch power of OFF transmitter, each lane	P_{off}			-30	dBm	
Encircled flux		≥ 86% at 19 μ m ≤ 30% at 4.5 μ m				
Transmitter eye mask definition		{0.23, 0.34, 0.43, 0.27, 0.35, 0.40}				1
Receiver						
Signaling Speed per Lane		10.3125 ± 100ppm			Gb/s	
Lane wavelengths (Range)	λ	840		860	nm	
Damage threshold		+3.4			dBm	
Average receiver power, each lane		-9.5		2.4	dBm	
Optical Modulation Amplitude (OMA), each lane	OMA			3	dBm	
Receiver sensitivity power in OMA, each lane	RXsen			-7.5	dBm	2

Notes:

1. Hit ratio = 5×10^{-5} per sample.
2. Measured with PRBS= $2^{31}-1$ @ 10.3125Gb/s

P Pin Descriptions (compliant SFF-8679)



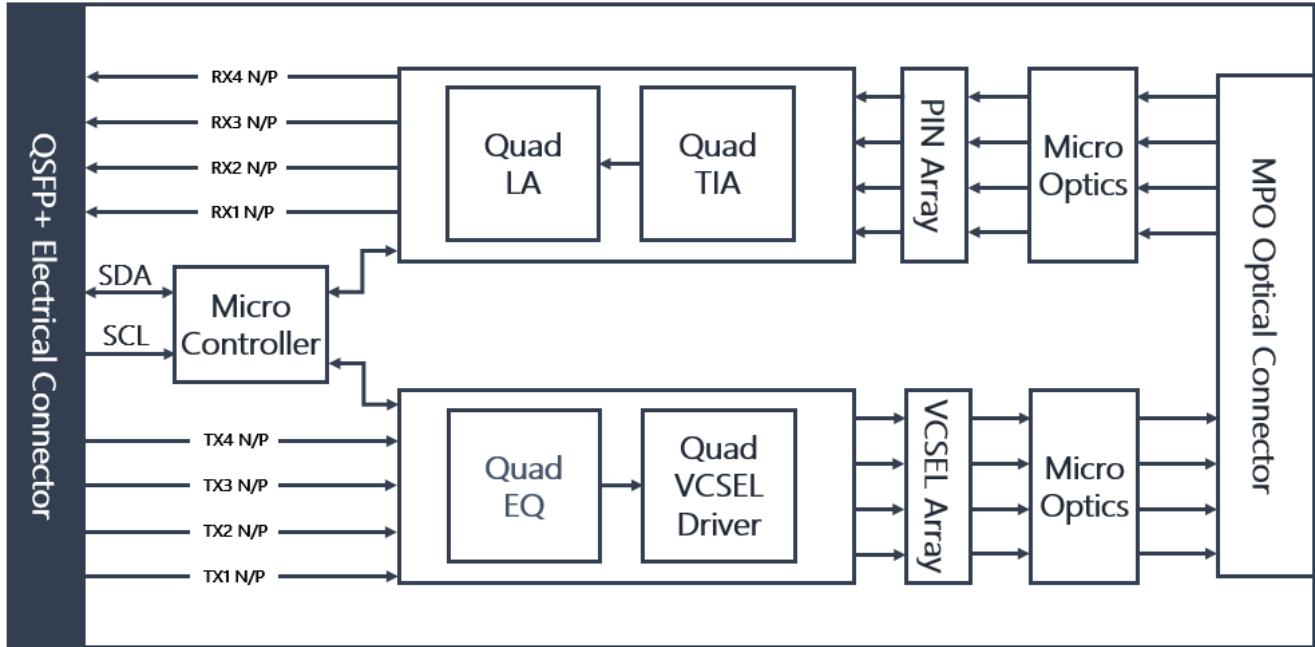
PIN	Symbol	Description	Ref.
1	GND	Ground	
2	TX2n	Transmitter Inverted Data Input	
3	TX2p	Transmitter Non-Inverted Data Input	
4	GND	Ground	1
5	TX4n	Transmitter Inverted Data Input	
6	TX4p	Transmitter Non-Inverted Data Input	
7	GND	Ground	1
8	ModSelL	Module Select	2
9	ResetL	Module Reset	2
10	V _{cc} RX	+3.3V Receiver Power Supply Receiver	
11	SCL	2-wire Serial Interface Clock	2
12	SDA	2-wire Serial Interface Data	2
13	GND	Ground	1
14	RX3p	Receiver Non-Inverted Data Output	
15	RX3n	Receiver Inverted Data Output	

16	GND	Ground	1
17	RX1p	Receiver Non-Inverted Data Output	
18	RX1n	Receiver Inverted Data Output	
19	GND	Ground	1
20	GND	Ground	1
21	RX2n	Receiver Inverted Data Output	
22	RX2p	Receiver Non-Inverted Data Output	
23	GND	Ground	1
24	RX4n	Receiver Inverted Data Output	
25	RX4p	Receiver Non-Inverted Data Output	
26	GND	Ground	1
27	ModPrsL	Module Present, internal pulled down to GND	
28	IntL	Interrupt output, should be pulled up on host board	
29	Vcc TX	+3.3V Transmitter Power Supply	
30	Vcc1	+3.3V Power Supply	
31	LPMODE	Low Power Mode	2
32	GND	Ground	
33	TX3p	Transmitter Non-Inverted Data Input	
34	TX3n	Transmitter Inverted Data Input	
35	GND	Ground	
36	TX1p	Transmitter Non-Inverted Data Input	
37	TX1n	Transmitter Inverted Data Input	
38	GND	Ground	1

Notes:

1. GND is the symbol for signal and supply (power) common for the module. All are common within the module and all module voltages are reference to this potential unless otherwise noted. Module circuit ground is isolated from module chassis ground within the module.
2. Open collector, should be pulled up with 4.7~10K ohms on the host board to a voltage between 3.15V and 3.6V.

Recommended Host Board Schematic



Memory map (compliant SFF-8636)

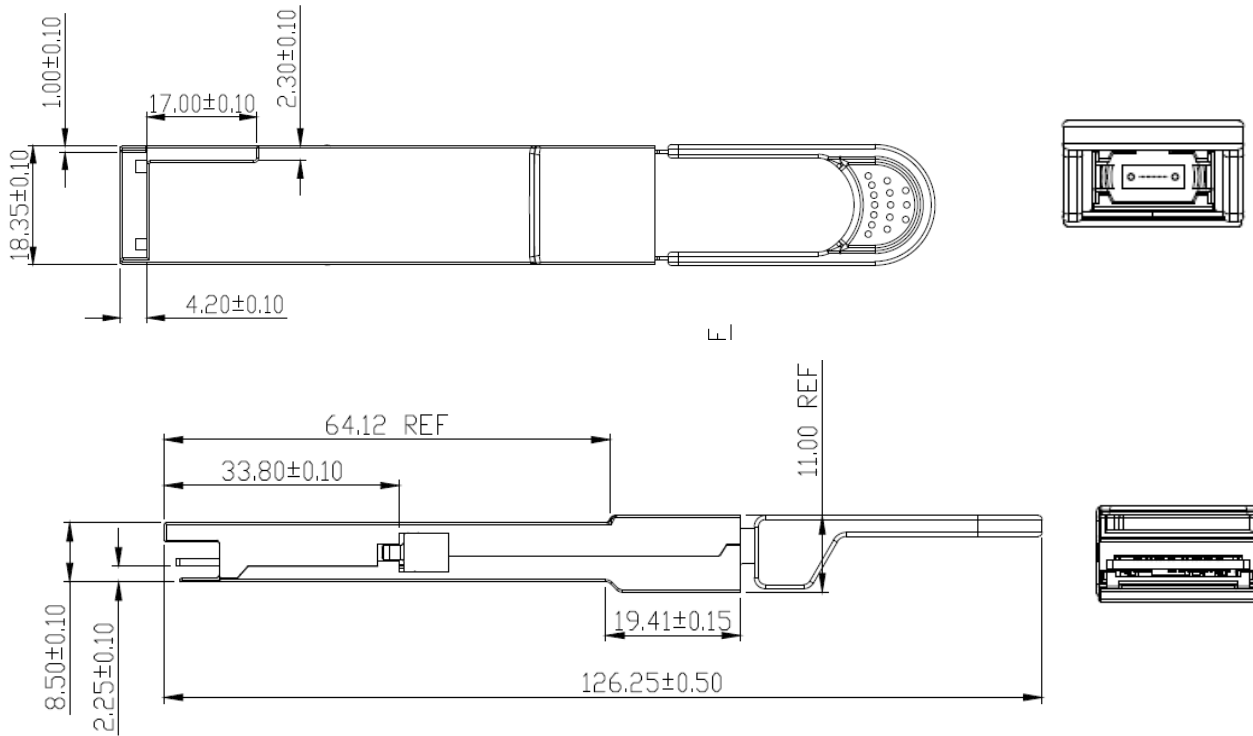
2-Wire Serial Address 1010000x	
Lower Page 00h	
0	Identifier
1 - 2	Status
3 - 2 1	Interrupt Flags
2 2 - 2 3	Free Side Device Monitors
3 4 - 8 1	Channel Monitors
8 2 - 8 5	Reserved
8 6 - 9 8	Control
99	Reserved
100-104	Hardware Interrupt Pin Masks
105-106	Vendor Specific
107	Reserved
108-110	Free Side Device Properties
111-112	Assigned for use by PCI Express
113	Free Side Device Properties
114-118	Reserved
119-122	Password Change Entry Area (Optional)
123-126	Password Entry Area (Optional)
127	Page Select Byte



Upper Page 00h	Optional Page 01h	Optional Page 02h	Optional Page 03h
128 Identifier	128 CC_APPS	128-255 User EEPROM data	128-175 Free Side Device Thresholds
129-191 Base ID Fields	129 AST Table Length (TL)		
	130-131 Application Code Entry 0		
	132-133 Application Code Entry 1		
	134-253 other entries		
192-223 Extended ID			176-223 Channel Thresholds
224-255 Vendor Specific ID			224 TX EQ & RX Emphasis Magnitude ID
			225 RX output amplitude indicators
			226-241 Channel Monitor Masks
	254-255 Application Code Entry TL		252-255 Reserved

Mechanical Drawing

Product shall be of design, construction and physical dimensions specified on applicable product drawing.



Unit: mm

Ordering Information

Part No	Specification									
	Package	Data rate per Lane	Laser	Optical Power	Detector	Max. Receive Sensitivity (OMA)	Temp	Reach	Other	Application code
WST-QSFP+SR4-C	QSFP+	10.3125 Gbps each Channel	850nm VCSEL	-7.6~ +2.4 each Channel	PIN	-7.5 dBm each Channel	0~70°C	100m via OM3; 150m via OM4	DDM RoHS	40GBASE-SR4 Ethernet

Modification History

Revision	Date	Description	Originator	Review	Approved
V1.0	21-Jun-2019	New Issue	Ivy Chen	Wayne Liao	Wayne Liao



Taipei Headquarters
16F-5, No. 75, Sec. 1,
Xintai 5th Rd., Xizhi
Dist., New Taipei City
22101, Taiwan
Tel: +886-2-2698-7208
Fax: +886-2-2698-7210

U.S. Branch
2080 Rancho Higuera Ct.
Fremont, CA 94539,
USA
Tel: 510-651-7800
Fax: 510-651-7822

ShenZhen Branch
610#, 6F, No.204
Building, 2nd Industrial
zone Nanyou, Nanshan
district, Shenzhen,
Guangdong China
518054
Tel: +86-755-86265980

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